

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	10/759483
				Filing Date	1/16/2004
				First Named Inventor	Rodney E. Hooker
				Group Art Unit	2186
				Examiner Name	Ryan Dare
				Attorney Docket Number	CNTR.2232
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
RD		6622211 (English-Language Equivalent of Chinese application CN1397887A published 02-19-2003)	9-16-2003	Henry et al.	

FOREIGN PATENT DOCUMENTS						
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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

Examiner Signature	/Ryan Dare/	Date Considered	08/21/2006
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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RD		CN1391167A (US20030159003A1) provided herewith as an English- Language Equivalent Application)	01-15-2003	Geisking et al.		

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RD		GB2307319	05-21-1997	Han Heung Kim		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RD		"Dual On-Chip Instruction Cache Organization in High Speed Processors." IBM Technical Disclosure Bulletin. IBM Corp. New York, US. Volume 37, No. 12, December 1994, pages 213-214.	

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RD		5500950	03-19-1996	Becker et al.	
RD		5751990	05-12-1998	Krolak et al.	
RD		5956752	09-21-1999	Gregory S. Mathews	
RD		6275903	08-14-2001	Koppala et al.	

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